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PATENT

IN THE U.S. PATENT & TRADEMARK OFFICE

Applicant:	CHIN-LONG CHEN	:	GROUP ART UNIT:
Serial No.:	TBD	:	Examiner:
Filed:	Herewith	:	October 9, 2001
Title:	BLOCK-SERIAL FINITE	:	Lawrence D. Cutter
FIELD MULTIPLIERS		:	IBM Corporation, IP Law Dept.
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INFORMATION DISCLOSURE STATEMENT


Assistant Commissioner for Patents
Washington, D. C. 20231

Sir:

Listed on the accompanying form PTO-1449 are documents that are being submitted for consideration under 37 CFR 1.97 and 1.98. Copies of the documents are enclosed.

Song and Parhi discuss a similar approach but do not employ intermediate processing mod $p(x)$. As a result, their storage requirements are higher.

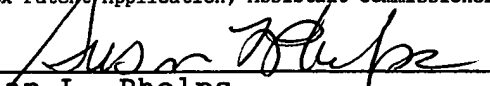
RESPECTFULLY SUBMITTED,


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EXAMINER **DATE CONSIDERED**

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form PTO-A820 (also form PTO-1449) P09C/REV03 Patent and Trademark Office * U.S. DEPARTMENT OF COMMERCE

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	APPLICANT(S) C. L. Chen Express Mail #ET089965542US	
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U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>			
		e	Song et al., Low-Energy Digit-Serial/Parallel Finite Field Multipliers, Journal of VLSI Signal Processing 19, Kluwer Academic Publishers, 1998, pp. 150-166.